



Analog IC Design with Low-Dropout Regulators, Second Edition

Gabriel Rincon-Mora

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THE LATEST ANALOG IC DESIGN TECHNIQUES

Fully revised and expanded to meet the emerging demands of mixed-signal systems, *Analog IC Design with Low-Dropout Regulators*, Second Edition, teaches analog IC concepts and explains how to use them to design, analyze, and build linear low-dropout (LDO) regulator ICs with bipolar, CMOS, and biCMOS semiconductor process technologies. The book draws physical insight from topics presented and illustrates how to develop and evaluate analog ICs for today's expanding wireless and mobile markets. Practical examples and end-of-chapter review questions reinforce important concepts and techniques developed in this cutting-edge guide.

LEARN HOW TO:

- Evaluate power-supply systems
- Predict and specify how linear regulators perform and respond to variations in their supplies, loads, and other working conditions
- Work with semiconductor devices--resistors, capacitors, diodes, and transistors
- Combine microelectronic components to design current mirrors, differential pairs, differential amplifiers, linear low-dropout regulators, and their variants
- Close and stabilize feedback control loops that regulate voltages and currents
- Design circuits that establish reliable bias currents and reference circuits
- Determine the small-signal dynamics of analog ICs and analog systems
- Establish independent, stable, noise-free, and predictable power-supply voltages
- Implement overcurrent, thermal, reverse-battery, and ESD protection
- Test, measure, and evaluate linear regulator ICs

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